



How we quadrupled the network speed world record: bandwidth Challenge at SC 2004

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Motivation and objectives

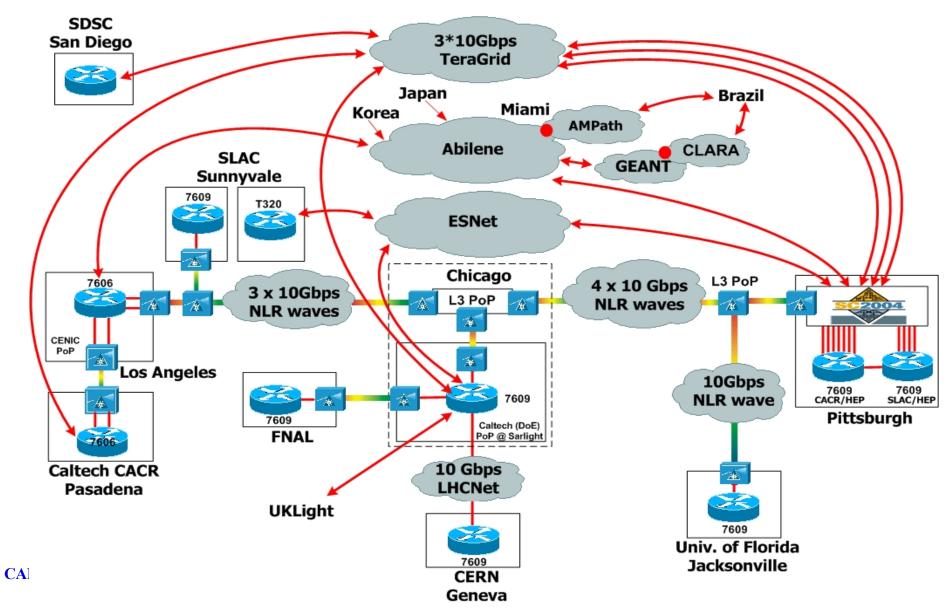


- High Speed TeraByte Transfers for Physics: onetenth of a terabits per second achieved (101 Gbps)
- Demonstrating that many 10 Gbps wavelengths can be used efficiently over various continental and transoceanic distances
- Preview of the globally distributed grid system that is now being developed in preparation for the next generation of high-energy physics experiments at CERN's Large Hadron Collider (LHC).
- Monitoring the WAN performance using MonALISA
- Major Partners : Caltech-FNAL-SLAC
- Major Sponsors:
 - ☐ Cisco, S2io, HP, Newysis
- Major networks:
 - NLR, Abilene, ESnet, LHCnet, AMPATH, TeraGrid



Network (I)

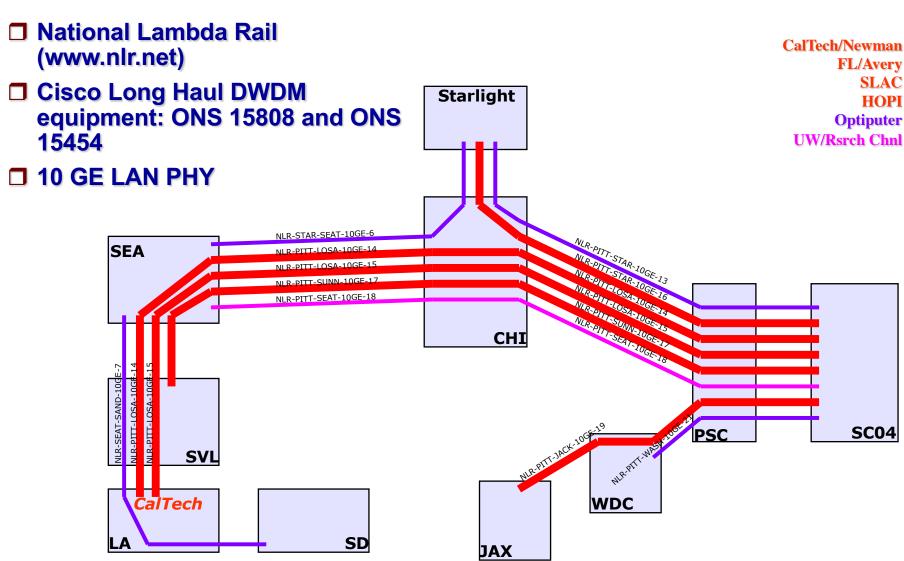






5 NLR Waves dedicated to HEP







Network (II)

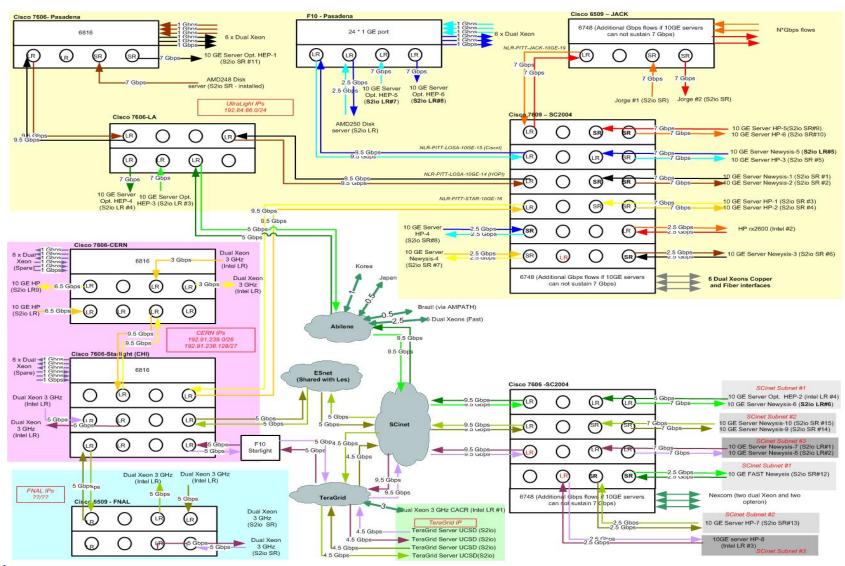


- **♦** 80 10GE ports
- ♦ 50 10GE network adapters
- ◆ 10 Cisco 7600/6500 dedicated to the experiment
- Backbone for the experiment built in two days
- Four dedicated wavelengths of NLR to Caltech booth
 - □ Los Angeles (2 waves),
 - □ Chicago
 - □ Jacksonville
- One dedicated wavelengths of NLR to SLAC-FNAL booth from Sunnyvale
- Connections (L3 peerings) to showfloor via SCinet:
 - ESnet
 - TeraGrid
 - □ Abilene
- Balance traffic between WAN links
 - UltraLight Autonomous system dedicated to the experiment (AS32361)
 - ☐ L2 10 GE connections to LA, JACK and CHI
 - Dedicated LSPs (MPLS Label Switch Path) on Abilene



Flows Matrix



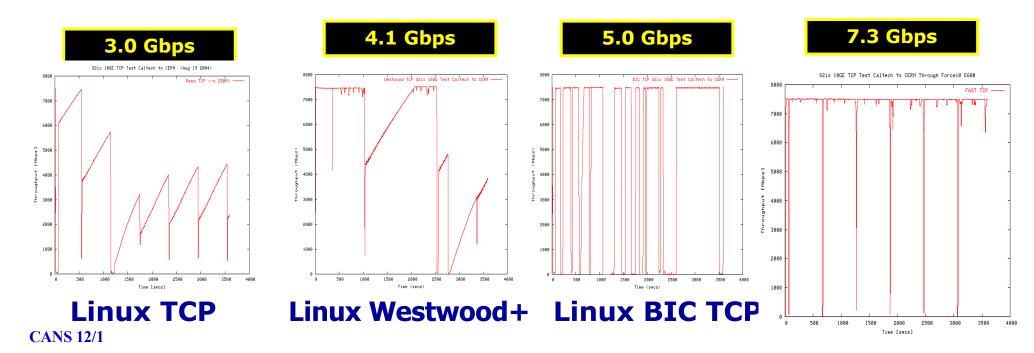




Selecting the data Transport protocol



- Tests between CERN and Caltech
- Capacity = OC-192 9.5Gbps; 264 ms round trip latency; 1 flow
- Sending station: Tyan S2882 motherboard, 2 x Opteron 2.4 GHz,
 2 GB DDR.
- ◆ <u>Receiving station:</u> (CERN OpenLab): HP rx4640, 4 x 1.5GHz Itanium-2, zx1 chipset, 8GB memory
- Network adapter: S2io 10 GE





Selecting 10 GE NICs



	S2io Adapter	Intel Adapter
Max. b2b TCP throughput	7.5 Gbps	5.5 Gbps
Rx Frame Buffer Capacity	64MB	256KB
MTU	9600Byte	16114Byte
IPv4 TCP Large Send Offload	Max offload size 64k	Partial; Max offload size 32k



♦ TSO

- Must have hardware support in NIC.
- ♦ It allows TCP layer to send a larger than normal segment of data, e,g, 64KB, to the driver and then the NIC. The NIC then fragments the large packet into smaller (<=mtu) packets.
- Benefits:
 - > TSO can reduce CPU overhead by 10%~15%.
 - Increase TCP responsiveness.

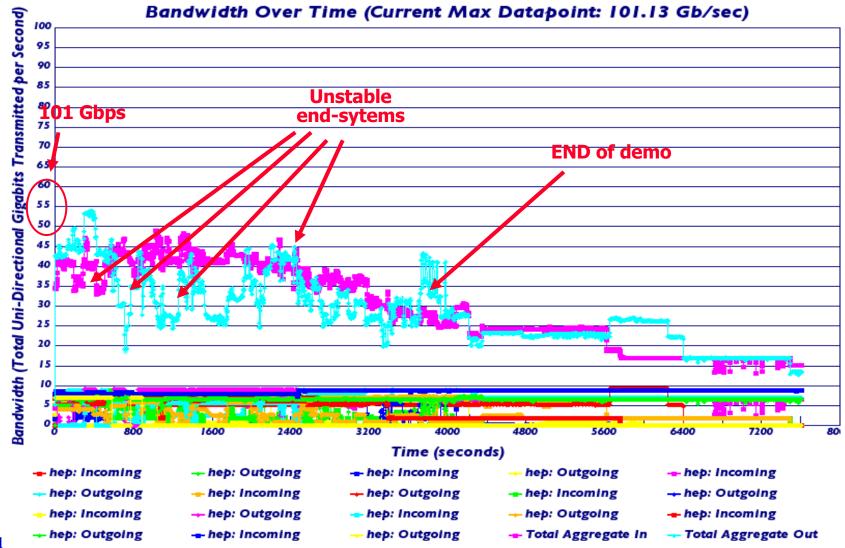
p=(C*RTT*RTT)/(2*MSS)

p: Time to recover to full rate C: Capacity of the link RTT: Round Trip Time MSS: Maximum Segment Size



101 Gigabit Per Second Mark





CANS 12/1

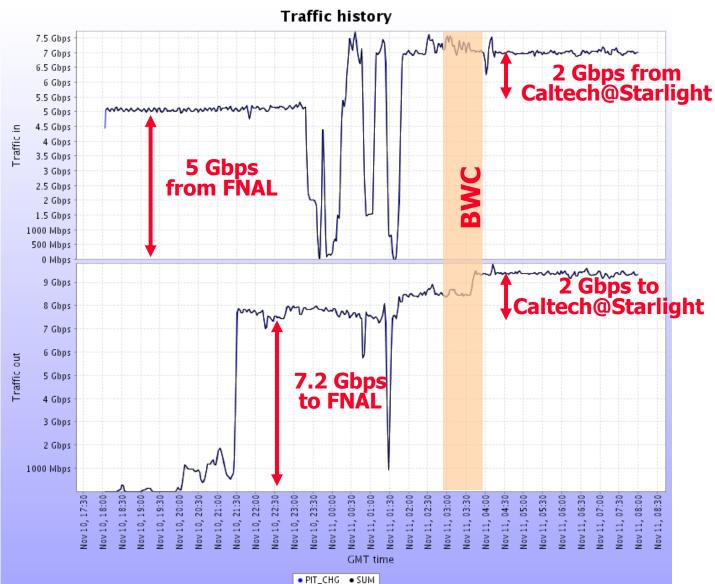
Source: Bandwidth Challenge committee



PIT-CHI NLR wave utilization



- **□ FAST TCP**
- □ Performance to/from FNAL are very stable
- ☐ Bandwidth utilization during BWC = 80 %

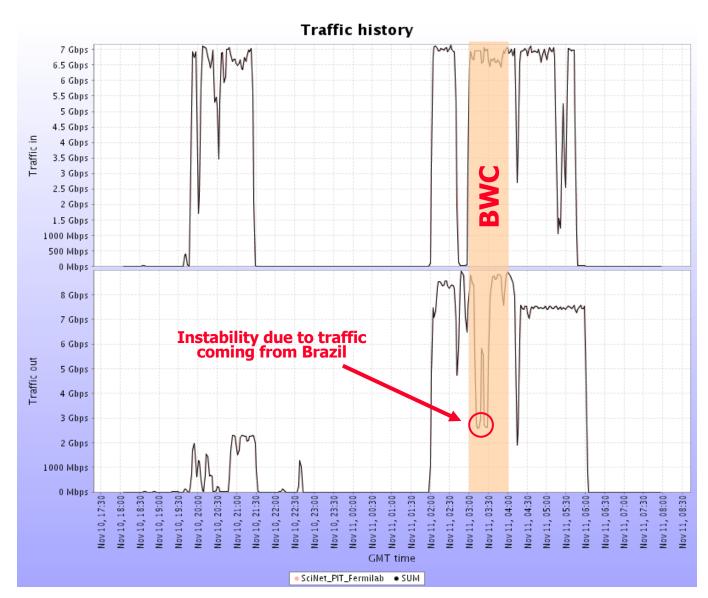




Abilene Wave #1



- ☐ FAST TCP
- ☐ Traffic to CERN via Abilene & CHI
- □ Bandwidth utilization during BWC = 65 %

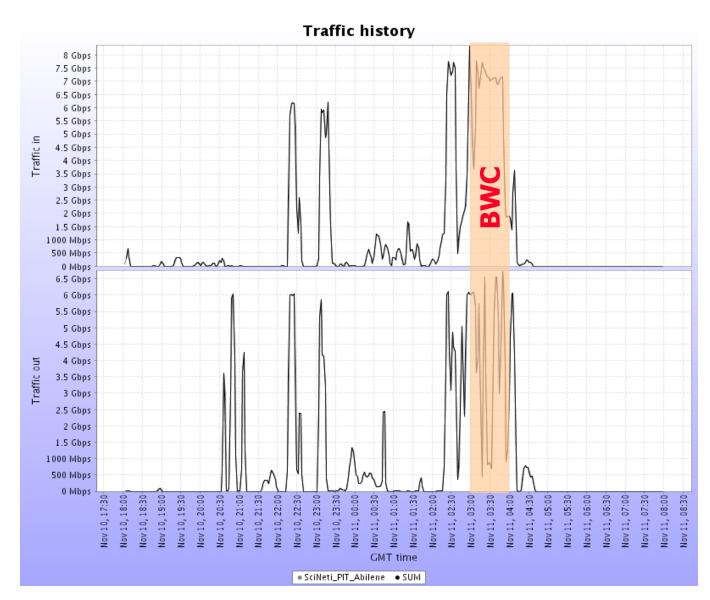




Abilene Wave #2



- □ TCP Reno
- ☐ Traffic to LA via
 Abilene & NY
- ☐ Bandwidth utilization during BWC = 52.5 %
- Max. throughput is better with RENO but FAST is more stable
- ☐ Each time a packet is lost, the RENO flow has to be restarted

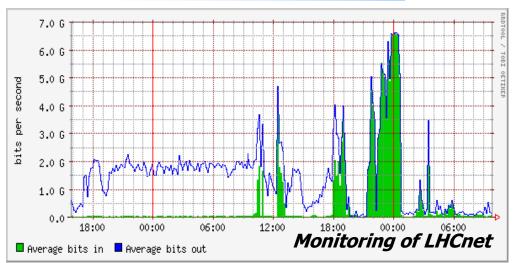


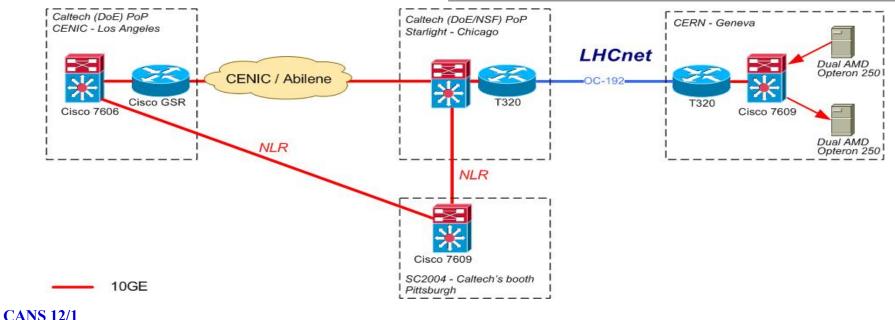


Internet 2 Land Speed record submission



- ☐ Multi-Streams 6.86 Gbps X 27 kkm
- ☐ PATH: GVA-CHI-LA-PIT-CHI-GVA
- ☐ The LHCnet is crossed in both directions
- ☐ RTT = 466 ms
- ☐ FAST TCP (18 streams)

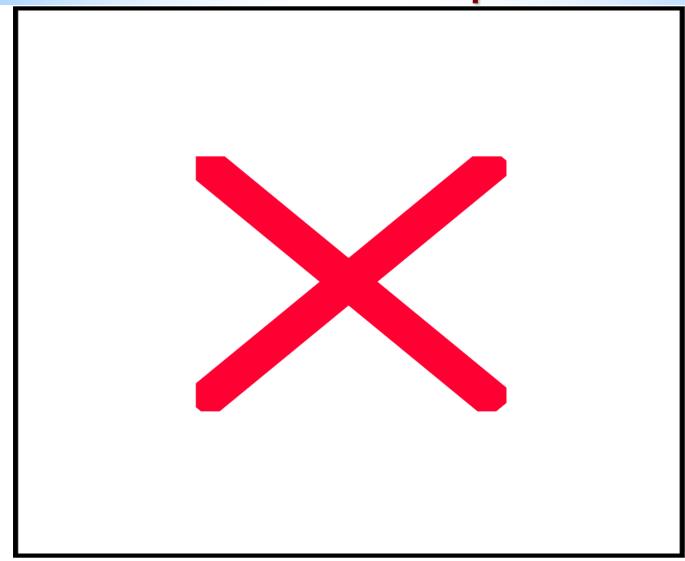






An internationally collaborative effort: Brazillian example

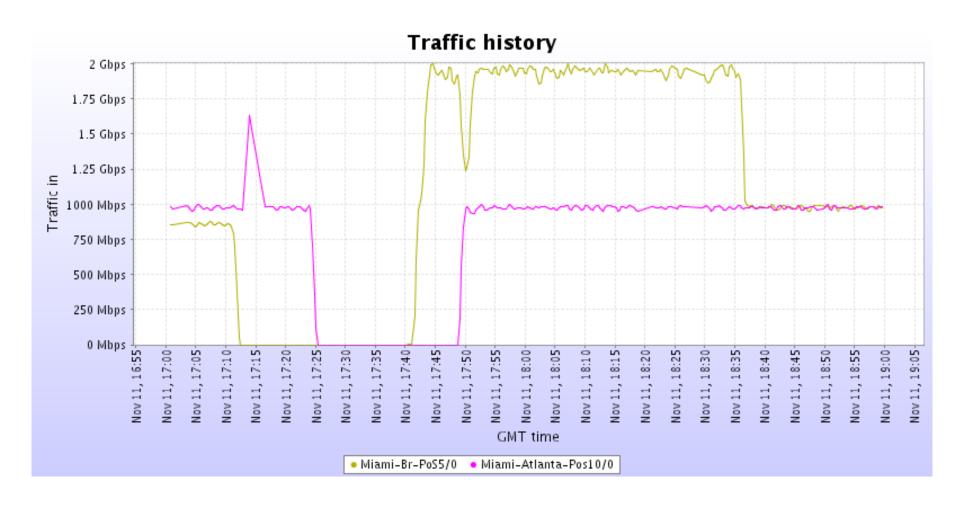






2 +1 Gbps network record on Brazillian network







A few lessons



- Logistics: server crashes, NIC driver problems, Last minutes Kernel tuning, router misconfiguration, optics issues.
- Embarrassment of the rich: mix and match 3 10G SciNet drops with 2 Abilene and 3 TeraGrid waves. Better planning.
- Mixing small flows (1Gbps) with large flows (10 Gbps) and different RTTs can be tricky.
 - Just filling in the leftover bandwidth?
 - Or negatively affect the large flows going over long distance?
 - Different level of FAST aggressiveness + packet losses on the path;
- Efficient topological design
 - ☐ instead of star-like topology to source-sink all traffic at the showfloor;
 - ☐ (routing traffic through a large router at showfloor but to remote servers.
- Systematic tuning of the servers
 - Server crashes main reason of performance problems;
 - 10GbE NIC drivers improvement;
 - Better ventilation at the show;
 - Using large default socket buffer versus setting large window by applications (iperf)



Partners



